

Tenth International Conference on Recent Trends in Information, Telecommunication and Computing ITC 2019

Feb 22-23, 2019; in Bengaluru, India
<http://itc.theides.org/2019/>



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Tenth International Conference on Recent Trends in Information, Telecommunication and Computing – ITC 2019, will be held during Feb 22-23, 2019; in Bengaluru, India, is to bring together innovative academics and industrial experts in the field of Computer Science, Information Technology, Computational Engineering, and Communication to a common forum.

The ITC conference is jointly organised by the IDES and the Association of Computer Electrical Electronics and Communication Engineers (ACEECom). All the accepted registered papers will be published by the Grenze Scientific Society and it will be made available in the *Grenze Digital Library*, and will be submitted to possible indexing in all the major indexing services like EI, ISI Web of Science, DBLP, IET Inspec, Scopus and etc.

Selected, registered papers will be published in the special issues of the following journals

GRENZE International Journal of Engineering and Technology (GIJET)
GRENZE International Journal of Computer Theory and Engineering (GIJCTE)

ITC 2019, is an international conference where theory, practices, and applications of Information Technology, Computational Engineering, Computer and Telecommunication Technology and related topics are presented and discussed. Original contributions are solicited on topics covered under broad areas such as (but not limited to):

Hardware/software interfaces	I/O and Data Communications
Modeling of computer architecture	Data Communications Devices
Systems specification methodology	Interfaces, Simulation, Sequential circuits
Processor Architectures	Parallel I/O, Verification
Single Data Stream Architectures	Web technologies, Parallel circuits
System architectures, integration and modeling	Wireless systems, Network Protocols
MISD processors, Pipeline processors	Performance Analysis and Design Aids
Mobile processors, Network topology	Register-Transfer-Level Implementation
RISC/CISC, VLIW architectures	Algorithm/protocol design and analysis
High-level language architectures	Network-level security and protection
Scheduling and task partitioning	Data-path design, Control design
Circuit-switching networks	Memory design, Error-checking
Packet-switching networks	Hardware description languages
Network communications	Computer Systems Organization
Distributed architectures	Optimization, Error-checking
Distributed architectures	Cellular arrays and automata
Parallel Architectures, Distributed networks	Memory control and access
Real-time distributed, Data communications	Reliability and Testing, Emerging technologies

Prospective authors are invited to submit full (original) research papers (which are NOT submitted/published/under consideration anywhere in other conferences/journals) in electronic (Doc or Docx) format through the CMT Conference Management System or via email itc.chair@gmail.com