

Eleventh International Conference on Recent Trends in Information, Telecommunication and Computing ITC 2020



Feb 28-29, 2020; in Bengaluru, India
<http://itc.theides.org/2020/>



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Eleventh International Conference on Recent Trends in Information, Telecommunication and Computing – ITC 2020, will be held during Feb 28-29, 2020; in Bengaluru, India, is to bring together innovative academics and industrial experts in the field of Computer Science, Information Technology, Computational Engineering, and Communication to a common forum.

The ITC conference is jointly organised by the IDES and the Association of Computer Electrical Electronics and Communication Engineers (ACEECom). All the accepted registered papers will be published by the Grenze Scientific Society and it will be made available in the *Grenze Digital Library*, and will be submitted to possible indexing in all the major indexing services like EI, ISI Web of Science, DBLP, IET Inspec, Scopus and etc.

Selected, registered papers will be published in the special issues of the following journals

GRENZE International Journal of Engineering and Technology (GIJET)
GRENZE International Journal of Computer Theory and Engineering (GIJCTE)

ITC 2020, is an international conference where theory, practices, and applications of Information Technology, Computational Engineering, Computer and Telecommunication Technology and related topics are presented and discussed. Original contributions are solicited on topics covered under broad areas such as (but not limited to):

Hardware/software interfaces
Modeling of computer architecture
Systems specification methodology
Processor Architectures
Single Data Stream Architectures
System architectures, integration and modeling
MISD processors, Pipeline processors
Mobile processors, Network topology
RISC/CISC, VLIW architectures
High-level language architectures
Scheduling and task partitioning
Circuit-switching networks
Packet-switching networks
Network communications
Distributed architectures
Distributed architectures
Parallel Architectures, Distributed networks
Real-time distributed, Data communications

I/O and Data Communications
Data Communications Devices
Interfaces, Simulation, Sequential circuits
Parallel I/O, Verification
Web technologies, Parallel circuits
Wireless systems, Network Protocols
Performance Analysis and Design Aids
Register-Transfer-Level Implementation
Algorithm/protocol design and analysis
Network-level security and protection
Data-path design, Control design
Memory design, Error-checking
Hardware description languages
Computer Systems Organization
Optimization, Error-checking
Cellular arrays and automata
Memory control and access
Reliability and Testing, Emerging technologies

Prospective authors are invited to submit full (original) research papers (which are NOT submitted/published/under consideration anywhere in other conferences/journals) in electronic (Doc or Docx) format through the CMT Conference Management System or via email itc.chair@gmail.com

