Twelfth International Conference on Recent Trends in Information, Telecommunication and Computing ITC 2021

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Twelfth International Conference on Recent Trends in Information, Telecommunication and Computing – ITC 2021, will be held during **Jan 27-28, 2021; in Bengaluru, India**, is to bring together innovative academics and industrial experts in the field of Computer Science, Information Technology, Computational Engineering, and Communication to a common forum.

ITC conference is jointly organised by the **IDES** and the Association of Computer Electrical Electronics and Communication Engineers (**ACEECom**). All the accepted registered papers will be published by the **Grenze Scientific Society** and it will be made available in the **GRENZE International Journal of Engineering and Technology (GIJET),** and will be indexed in **Scopus**. The primary goal of the conference is to promote research and developmental activities in Computer Science, Information Technology, Computational Engineering, and Communication. Another goal is to promote scientific information interchange between researchers, developers, engineers, students, and practitioners working in India and abroad.

ITC 2021, is an international conference where theory, practices, and applications of Information Technology, Computational Engineering, Computer and Telecommunication Technology and related topics are presented and discussed. Original contributions are solicited on topics covered under broad areas such as (but not limited to):

Hardware/software interfaces	I/O and Data Communications
Modeling of computer architecture	Data Communications Devices
Systems specification methodology	Interfaces, Simulation
Processor Architectures	Parallel I/O, Verification
Single Data Stream Architectures	Web technologies, Parallel circuits
System architectures, integration and modeling	Wireless systems, Network Protocols
MISD processors, Pipeline processors	Performance Analysis and Design Aids
Mobile processors, Network topology	Register-Transfer-Level Implementation
RISC/CISC, VLIW architectures	Algorithm/protocol design and analysis
High-level language architectures	Network-level security and protection
Scheduling and task partitioning	Data-path design, Control design
Circuit-switching networks	Memory design, Error-checking
Packet-switching networks	Hardware description languages
Network communications	Computer Systems Organization
Distributed architectures	Optimization, Error-checking
Distributed architectures	Cellular arrays and automata
Parallel Architectures	Memory control and access
Real-time distributed	Reliability and Testing
Data communications	Emerging technologies
Distributed networks	Sequential circuits

Prospective authors are invited to submit full (original) research papers (which are NOT submitted/published/under consideration anywhere in other conferences/journals) in electronic (PDF only) format through the **CMT Conference Management System** or via email **itc.chair@gmail.com**

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